

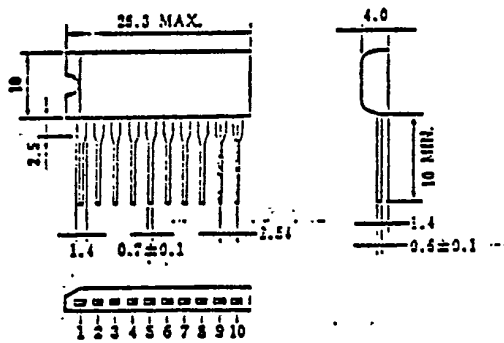


PRELIMINARY SPECIFICATION  
MOS FIELD EFFECT POWER TRANSISTOR ARRAY

μPA1552H

FAST SWITCHING  
N-CHANNEL SILICON POWER MOS FET ARRAY

PACKAGE DIMENSIONS  
In millimeters



2,4,6,8:Gate  
3,5,7,9:Drain  
1,10:Source

FEATURES

- Suitable for switching power supplies, actuator controls, and pulse circuits
- Low RDS(on)
- No second breakdown

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Drain to Source Voltage	VDS	60V
Gate to Source Voltage	VGS	±20V
Continuous Drain Current	ID(DC)	5A
Total Power Dissipation	PT	3.5W
Total Power Dissipation	PT*	22W
Channel Temperature	Tch	150°C
Storage Temperature	Tstg	-55~150°C
		*Tc=25°C

ELECTRICAL CHARACTERISTICS (Ta=25°C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Leakage Current	IDSS			10	μA	VDS=60V, VGS=0
Gate to Source Leakage Current	IGSS			±100	nA	VGS=±20V, VDS=0
Gate to Source Cutoff Voltage	VGS(off)	1.0		2.5	V	VDS=10V, ID=1mA
Forward Transfer Admittance	yfs	5			S	VDS=10V, ID=2A
Drain to Source On-State Resistance	RDS(on)		0.11	0.30	Ω	VGS=10V, ID=5A
			0.17	0.25	Ω	VGS=4V, ID=5A
Input Capacitance	Ciss		900		pF	VDS=10V
Output Capacitance	Coss		350		pF	VGS=0
Reverse Transfer Capacitance	Crss		50		pF	f=1MHz
Turn-On Delay Time	td(on)		10		ns	ID=2A, VGS=50V
Rise Time	tr		40		ns	VGS(on)=10V
Turn-Off Delay Time	td(off)		110		ns	RL=17 Ω
Fall Time	tf		20		ns	Rin=10 Ω

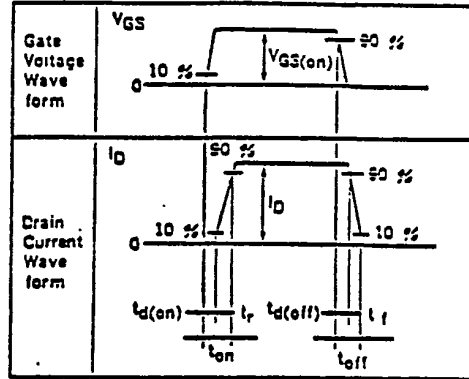
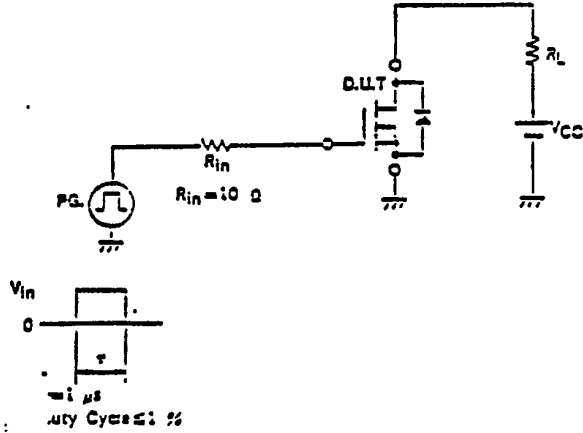
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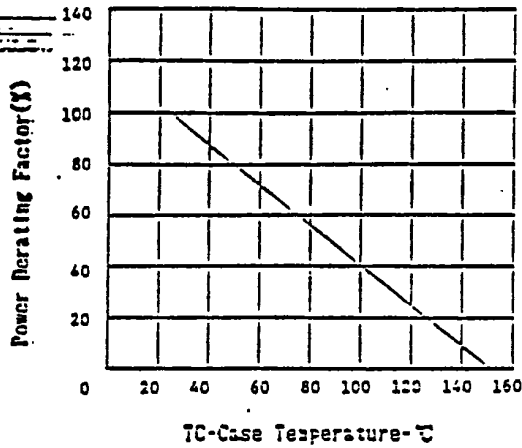
UPA1552H

NEC ELECTRON DEVICE

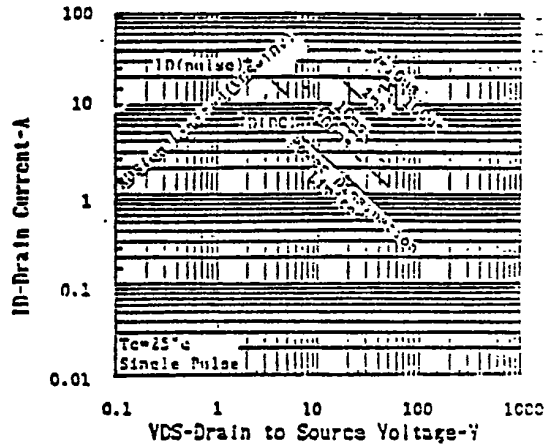
TURN-ON AND TURN-OFF TIME TEST CIRCUIT



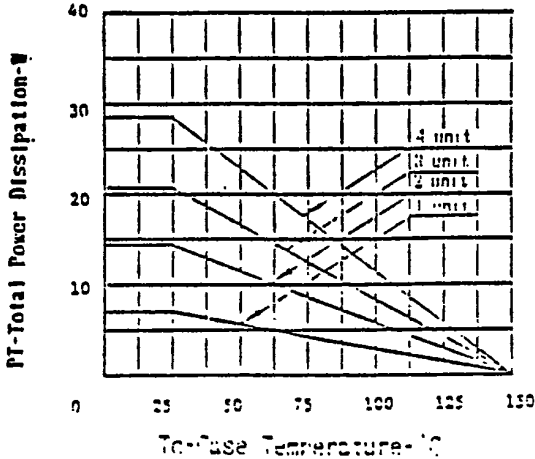
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



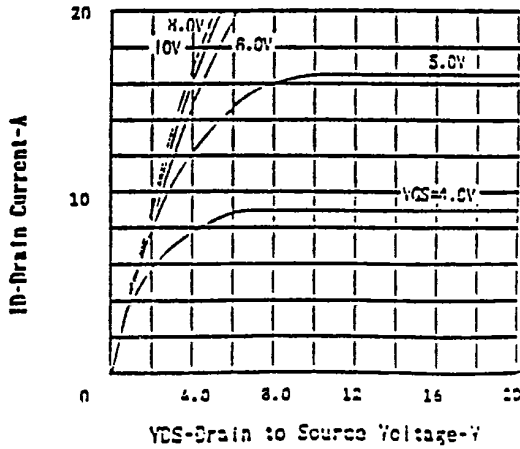
FORWARD BIAS SAFE OPERATING AREA



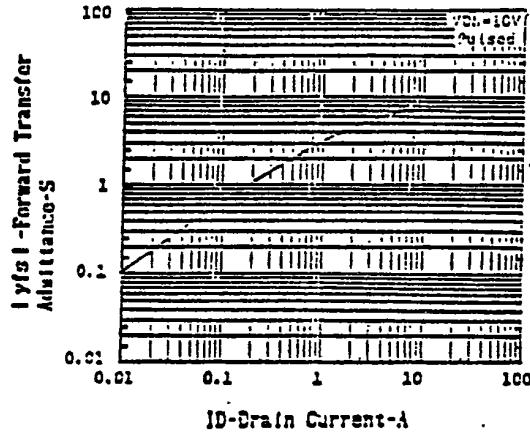
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



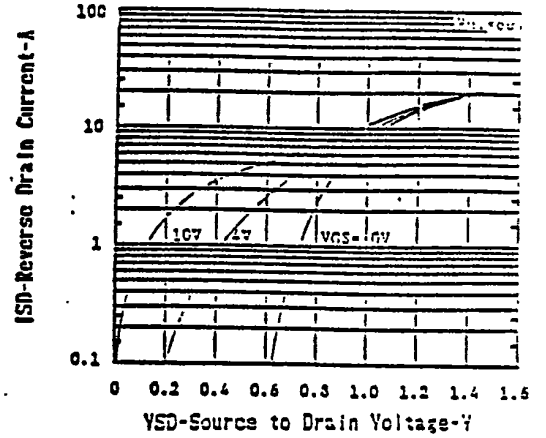
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



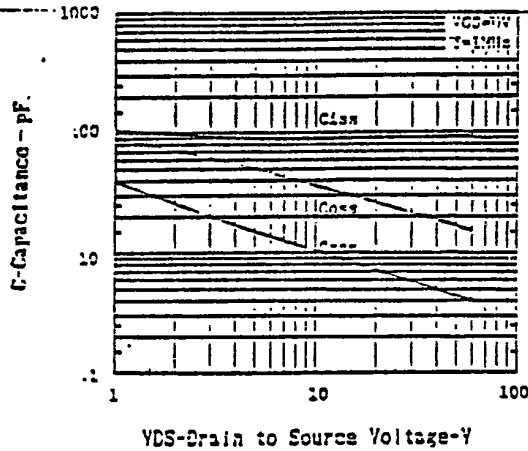
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



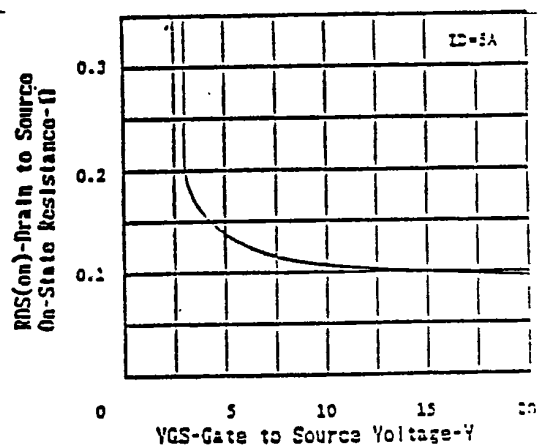
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



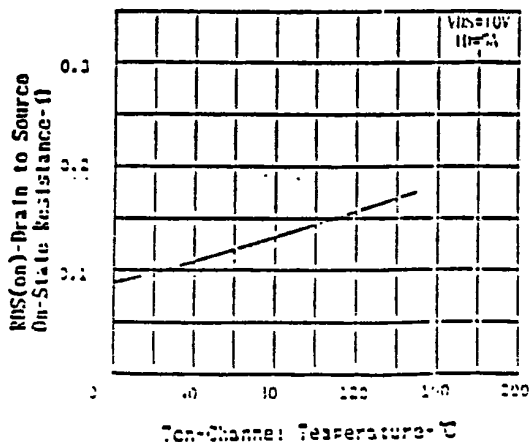
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



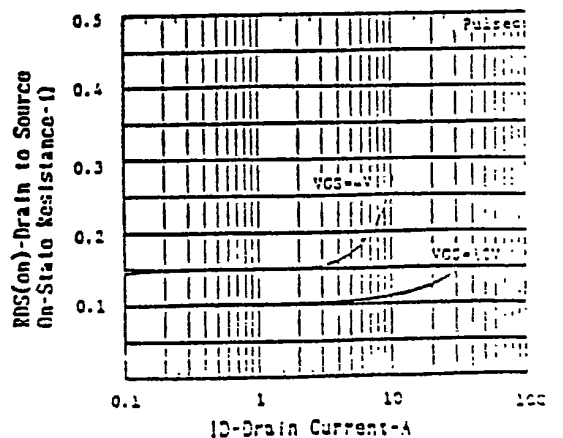
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



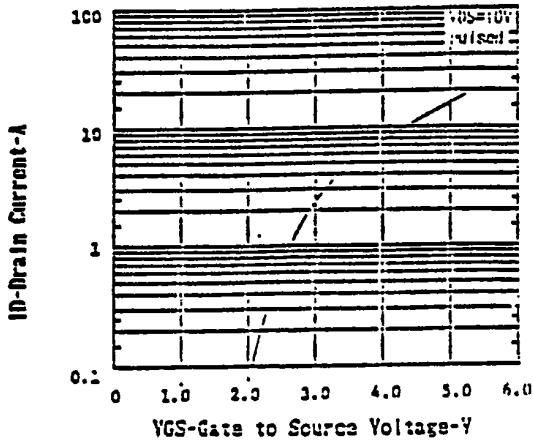
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



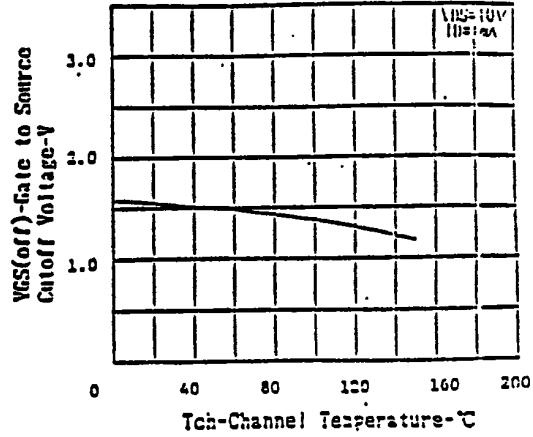
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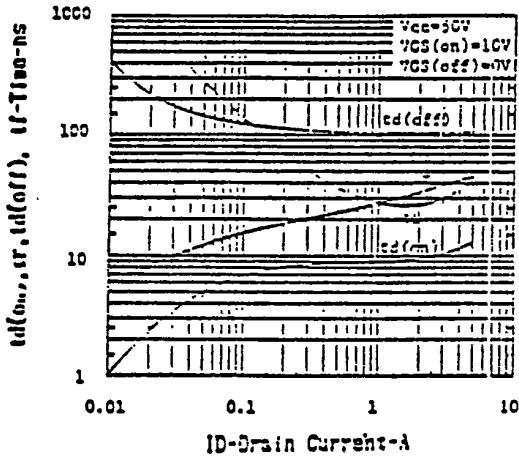
TRANSFER CHARACTERISTICS



GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE



TURN-ON AND TURN-OFF TIME



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